

<IGBT Modules>

CM300DX-24T/CM300DXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE



- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pin terminals



- Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

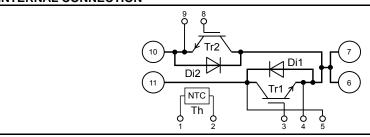
AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V_{CEsat} selection for parallel connection

INTERNAL CONNECTION

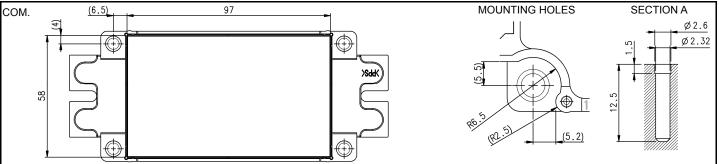


TERMINAL CODE

1. TH1 6. C2E1 2. TH2 7. C2E1 3. G1 8. G2 4. Es1 9. Es2 5. Cs1 10. E2 11. C1

OUTLINE DRAWING

Dimension in mm

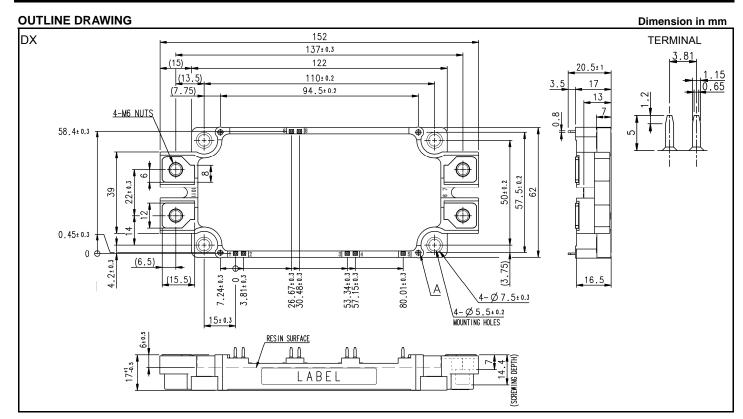


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INSULATED TYPE

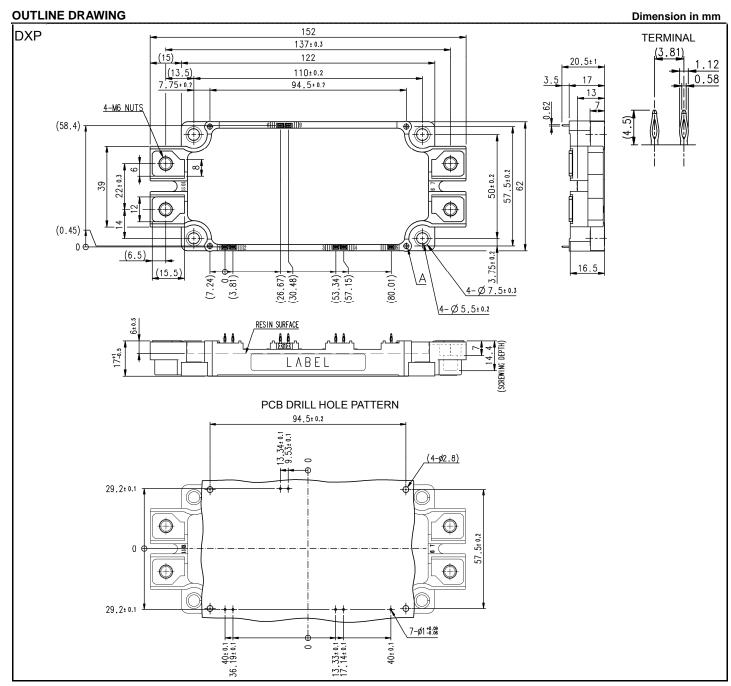


Tolerance otherwise specified

Divisio	n of l	Tolerance		
0.5		to 3		±0.2
over	over 3		6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

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Tolerance otherwise specified

Division of Dimension				Tolerance
0.5		to	3	±0.2
over	over 3		6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

HIGH POWER SWITCHING USE

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MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T _C =114 °C (Note2, 4)	300	۸	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	600	_ A	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	1700	W	
l _E (Note1)	Emitter current	DC (Note2)	300	_	
I _{ERM} (Note1)	Emilier current	Pulse, Repetitive (Note3)	600	A	

MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T_{Cmax}	Maximum case temperature	(Note4)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{sta}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T $_{\nu j}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

C. mahaal	He	Conditions			Limits		Unit
Symbol	Item			Min.	Тур.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited			-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =30 mA, V _{CE} =10 V		5.4	6.0	6.6	V
V _{CEsat} (Terminal)		I _C =300 A, V _{GE} =15 V,	T _{vj} =25 °C	-	1.60	2.00	
		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.80	-	V
	Only the second	(Note5)	T _{vj} =150 °C	-	1.85	-	
	Collector-emitter saturation voltage	I _C =300 A,	T _{vj} =25 °C	-	1.50	1.75	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	1.70	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	1.75	-	
Cies	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	72.8	
C_{oes}	Output capacitance			-	-	2.1	nF
Cres	Reverse transfer capacitance			-	-	0.9	
Q _G	Gate charge	V _{CC} =600 V, I _C =300 A, V _{GE} =15 V		-	2.26	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =300 A, V _{GE} =±15 V, R _G =1.6 Ω, Inductive load		-	-	600	ns
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	
t _f	Fall time			-	-	400	
Note1		I _E =300 A, G-E short-circuited,	T _{vj} =25 °C	-	1.60	2.20	V
V _{EC} (Note1) (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	1.75	-	
(Terrillial)	Emitter-collector voltage	(Note5)	T _{vj} =150 °C	-	1.80	-	
Note1)	- Emilier-collector voltage	I _E =300 A,	T _{vj} =25 °C	-	1.50	1.85	V
V _{EC} (Note1) (Chip)		G-E short-circuited,	T _{vj} =125 °C	-	1.50	-	
(Criip)		(Note5)	T _{vj} =150 °C	-	1.50	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =600 V, I _E =300 A, V _{GE} =±15 V,		-	-	400	ns
Q _{rr} (Note1)	Reverse recovery charge	R _G =1.6 Ω, Inductive load		-	23.4	-	μC
Eon	Turn-on switching energy per pulse	V_{CC} =600 V, I_{C} = I_{E} =300 A, V_{GE} =±15 V, R_{G} =1.6 Ω , T_{vj} =150 °C,		-	35	-	m l
E _{off}	Turn-off switching energy per pulse			-	30.7	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	20.5	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =2	5 °C (Note4)	-	0.88	-	mΩ
r _g	Internal gate resistance	Per switch		-	1.0	-	Ω

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HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
	item		Min.	Тур.	Max.	Offic
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions			Unit		
Syllibol	item			Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance Junction to case, per Inverter IGBT (Note4)				-	88	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	115	N/KVV
Б	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7)	-	11.5	-	K/kW
$R_{th(c-s)}$		per 1 module,	PC-TIM applied (Note4, 8)	-	3.1	-	r\/KVV

MECHANICAL CHARACTERISTICS

Cumahaal	lte m	Con		I Imit			
Symbol	Item	Con	ditions	Min.	Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
		Coldennin tune (DV)	Terminal to terminal	17	-	-	- mm
.1	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Terminal to base plate	16.8	-	-	mm
		Solder pin type (DX)	Terminal to terminal	10	-	-	mm
.1	Olassasas		Terminal to base plate	16.2	-	-	
d _a	Clearance	Description to the COVID	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP) Terminal to base plate		16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note9)		±0	-	+200	μm
m	mass	-		-	300	-	g

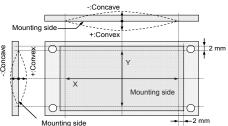
- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (Tvj) dose not exceed Tvjmax rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K)/D_(C-S)=50 μ m.
- 8. Typical value is measured by using PC-TIM of λ =3.4 W/(m·K)/D_(C-S)=50 μ m.
- 9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



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Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. PCB thickness: t=1.6.

	Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.73 ± 0.073 N•III	

RECOMMENDED OPERATING CONDITIONS

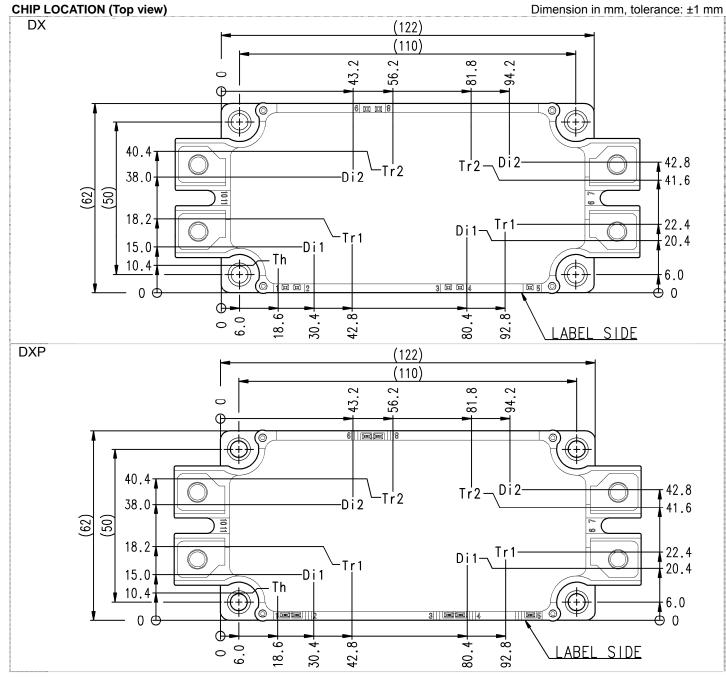
Symbol	Item	Conditions		Unit		
Symbol	item	Conditions	Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	1.6	-	16	Ω

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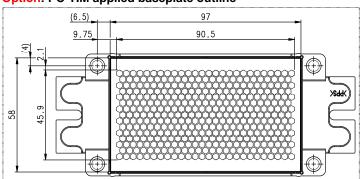
HIGH POWER SWITCHING USE

INSULATED TYPE



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

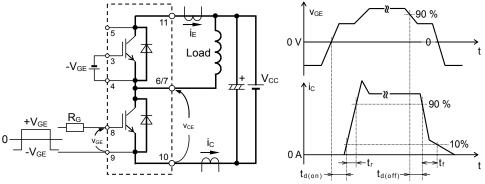
Option: PC-TIM applied baseplate outline

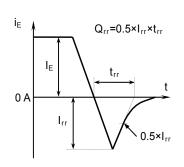


HIGH POWER SWITCHING USE

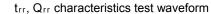
INSULATED TYPE

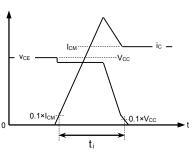
TEST CIRCUIT AND WAVEFORMS

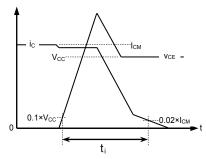


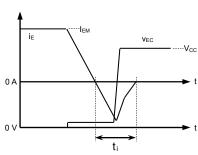


Switching characteristics test circuit and waveforms









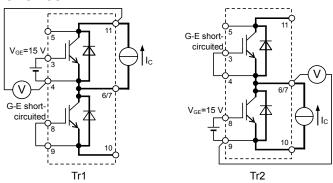
IGBT Turn-on switching energy

IGBT Turn-off switching energy

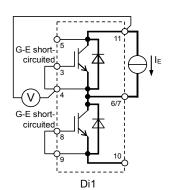
FWD Reverse recovery energy

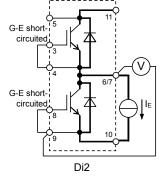
Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT









V_{EC} characteristics test circuit

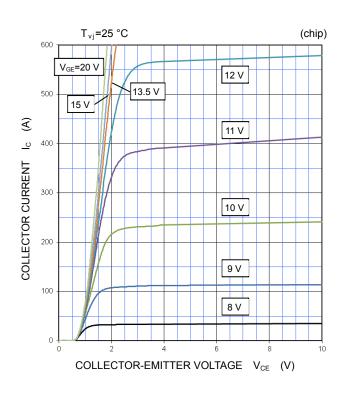
HIGH POWER SWITCHING USE

INSULATED TYPE

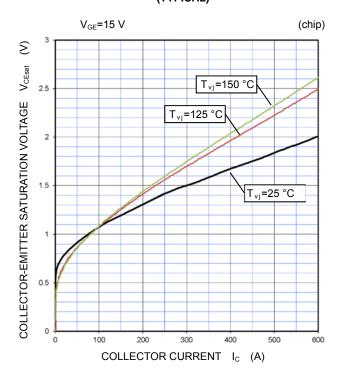
PERFORMANCE CURVES

INVERTER PART

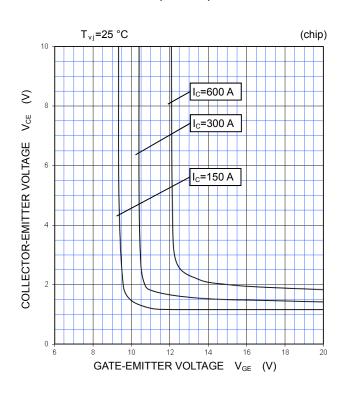
OUTPUT CHARACTERISTICS (TYPICAL)



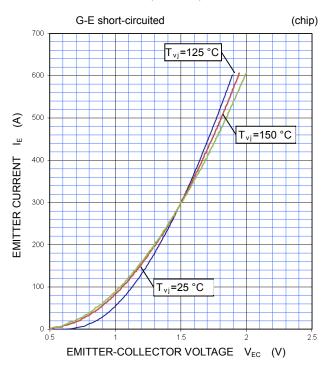
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



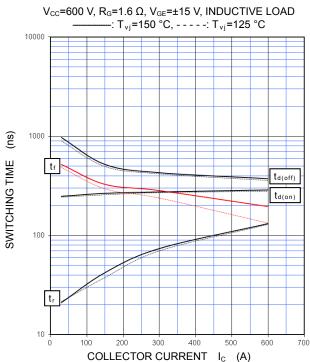
HIGH POWER SWITCHING USE

INSULATED TYPE

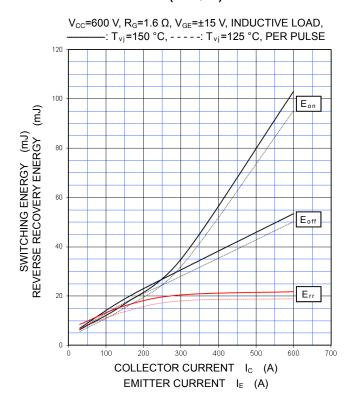
PERFORMANCE CURVES

INVERTER PART

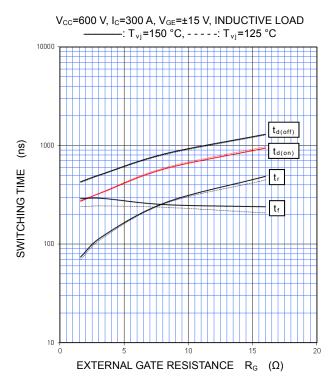
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



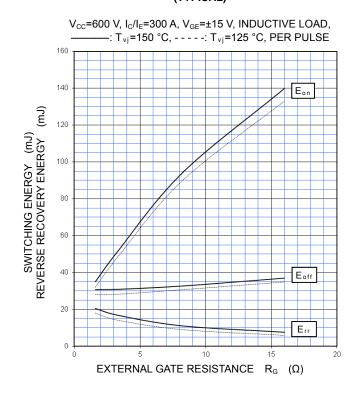
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



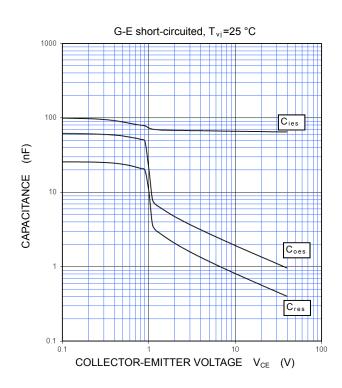
HIGH POWER SWITCHING USE

INSULATED TYPE

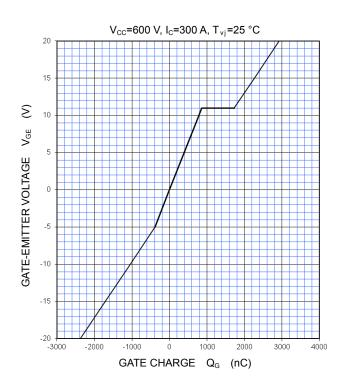
PERFORMANCE CURVES

INVERTER PART

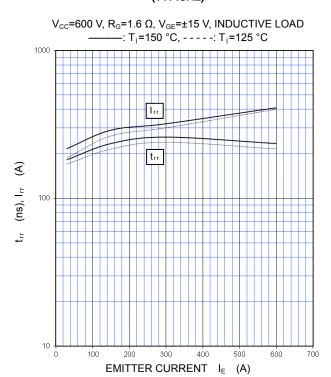
CAPACITANCE CHARACTERISTICS (TYPICAL)



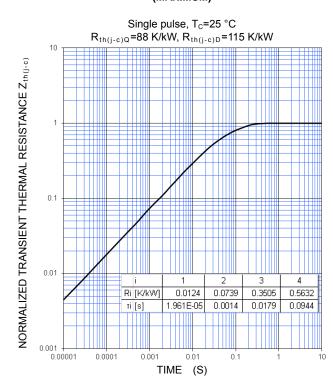
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

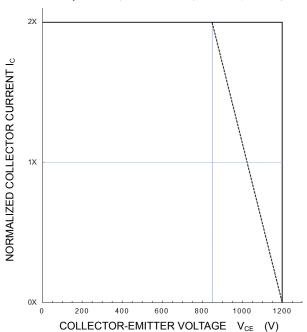
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

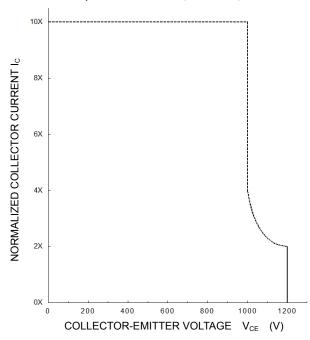
TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 $V_{\text{CC}} \le 850 \text{ V}$, $R_{\text{G}} = 1.6 \sim 16 \Omega$, $V_{\text{GE}} = \pm 15 \text{ V}$,: $T_{\text{V}_{\text{J}}} = 25 \sim 150 \,^{\circ}\text{C}$ (Normal load operations (Continuous): $T_{\text{V}_{\text{J}}} = 175 \,^{\circ}\text{C}$ (Unusual load operations (Limited period)



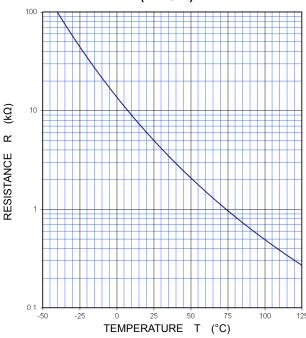
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 800 \text{ V}$, $R_G = 1.6 \sim 16 \Omega$, $V_{GE} = \pm 15 \text{ V}$, $T_{vj} = 25 \sim 150 \text{ °C}$, $t_W \le 8 \mu \text{s}$, Non-Repetitive



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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